

the same reading and caching, and involves multiple requests, not a single memory transaction.

Claim 1 also specifies a system for maintaining identity of which device, if any, owns the group of lines, and claim 7 specifies maintaining ownership information for the group of lines, and in each of these claims this group identity or ownership information is separate from ownership of the individual lines. The examiner cites Mackenthun *et al.*, column 12, lines 26-30 and column 2, lines 53-66. Column 12, lines 26-30 has nothing to do with ownership, or groups of lines. Column 2, lines 53-66 describes a directory based system, but the discussion has nothing to do with groups of lines.

In the paper mailed 10/18/2006, claims 1 and 7 were rejected under 35 USC 102(e) as being anticipated by US Patent Number 6,711,662 (Peir *et al.*). Applicant respectfully traverses.

Claim 1 specifies a cache memory that reads and caches a group of lines with a single memory transaction and claim 7 specifies retrieving a group of lines in response to a request for a single line. The examiner cites Peir *et al.* column 4, lines 33-35. The cited lines refer to pre-fetching of a data block. From Peir *et al.*, column 1, line 56, the term "block" is the same as "line". That is, the cited text describes pre-fetching of a line, not a group of lines.

Claim 1 also specifies a system for maintaining identity of which device, if any, owns the group of lines, and claim 7 specifies maintaining ownership information for the group of lines, and in each of these claims this group identity or ownership information is separate from ownership of the individual lines. The examiner cites Peir *et al.*, column 4, lines 51-67 and figure 3B. Nothing in the cited text deals with groups of lines. Note from column 4, line 41, "data X" is a block.

In the paper mailed 10/18/2006, claims 1 and 7 were rejected under 35 USC 102(e) as being anticipated by US Patent Number 5,615,334 (Wang *et al.*). Applicant respectfully traverses.

Claim 1 specifies a cache memory that reads and caches a group of lines with a single memory transaction and claim 7 specifies retrieving a group of lines in response to a request for a single line. The examiner cites Wang *et al.*, column 12, lines 23-58. Column

12, line 27 describes a sequence of read commands, not a single memory transaction as specified in claim 1, or a request for a single line as specified in claim 7.

Claim 1 also specifies a system for maintaining identity of which device, if any, owns the group of lines, and claim 7 specifies maintaining ownership information for the group of lines, and in each of these claims this group identity or ownership information is separate from ownership of the individual lines. The examiner cites Wang *et al.*, column 12, lines 23-58, and notes that Wang indicates ownership of data lines. Nothing in the cited text or elsewhere in Wang *et al.* teaches or suggests ownership of groups of lines separate from ownership of individual lines.

This application is considered to be in condition for allowance and such action is earnestly solicited.

Respectfully submitted,

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